F rm 1449 (M difie...) Inf rmati n Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. ALTRP116/A1364 Applicant: Pritchard et al. Filing Date February 9, 2004	Application No.: 10/775,966 Group 2811 ⅛25

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A1						
	A2						
	A3						
	A4						
	A5						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	No
	B1							
	B2							
	B3							
	B4							
	B5							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
VS	C1	Andrew DeHon, "Notes on Coupling Processors with Reconfigurable Logic", MIT Transit Project, Transit Note #118, downloaded from www.cs.caltech.eduresearch/ic/transit/tnl18/tn118.html March 1995, 29 pages
	C2	
	C3	
Examiner VUTHE SIEK		Date Considered 5/16/06

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.